

FRAMEWORK FOR ACCURATE DESIGN RULE CHECKING

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FIG. 1A

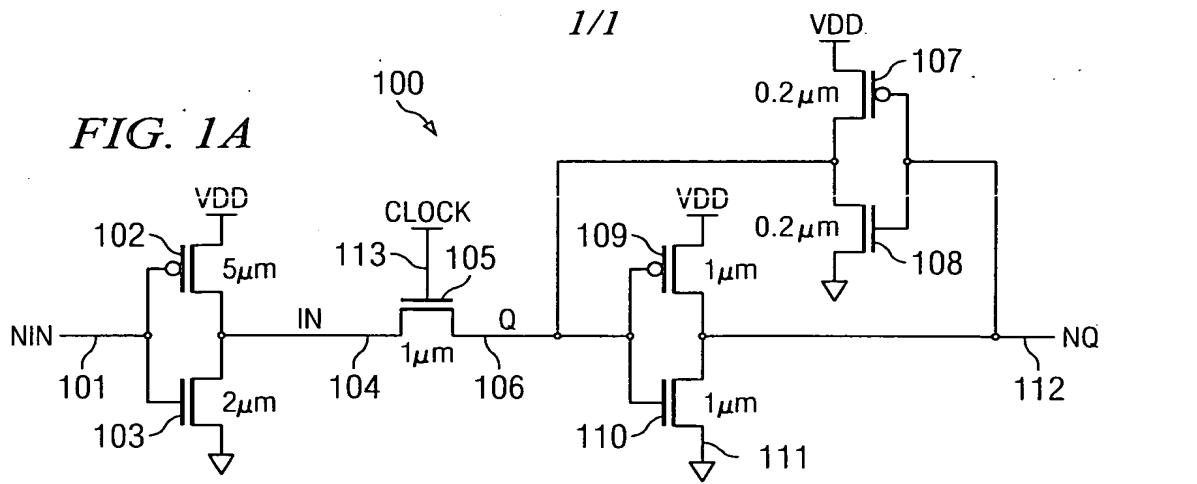


FIG. 1B

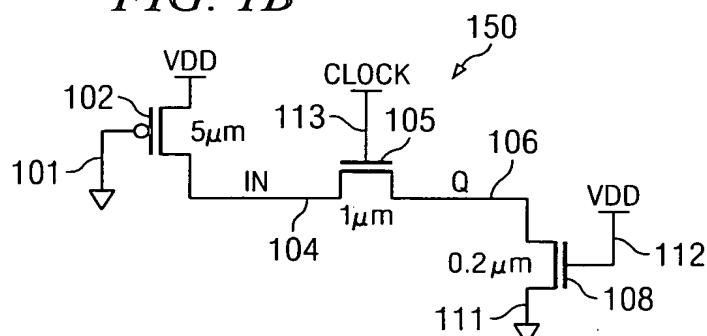


FIG. 2

FIG. 2 shows a table diagram labeled 200. The table has two columns: 'NFET FEEDBACK' and 'PASS FET'. The 'NFET FEEDBACK' column has values 0.1, 0.3, 0.5, ..., 5.0 μm. The 'PASS FET' column has values 0.75, 1.5, 3.0, ..., 10 μm. The table is enclosed in a frame with a top border 201, left border 202, right border 203, bottom border 204, and side borders 205 and 206. The output Q is connected to the right side of the table.

NFET FEEDBACK	PASS FET
0.1	0.9 0.95 0.98 ...
0.3	0.8 0.85 0.88 ...
0.5	0.7 0.75 0.78 ...
...	...
5.0 μm	...

FIG. 3

